NCX2200

Low voltage comparator

Rev. 1 — 22 March 2011

Product data sheet

1. General description

The NCX2200 provides a single low voltage low power comparator.

The NCX2200 has a very low supply current of 6 μ A and is guaranteed to operate at a low voltage of 1.3 V and is fully operational up to 5.5 V which makes this device convenient for use in both 3.0 V and 5.0 V systems.

2. Features and benefits

- Wide supply voltage range from 1.3 V to 5.5 V (functional operating range)
- Rail-to-rail input/output performance
- Very low supply current of 6 μA (typical)
- Very low-power consumption
- No phase inversion with overdriven input signals
- Internal hysteresis
- Propagation delay of 0.8 μs (typical)
- ESD protection:
 - ♦ HBM JESD22-A114F Class 3A. Exceeds 2000 V
 - ◆ CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from –40 °C to +85 °C

3. Applications

- Cellular telephones
- Alarm and security systems
- Personal Digital assistants



Low voltage comparator

4. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
NCX2200GW	–40 °C to +85 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
NCX2200GM	–40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886

^[1] Lead pitch is 0.4 mm.

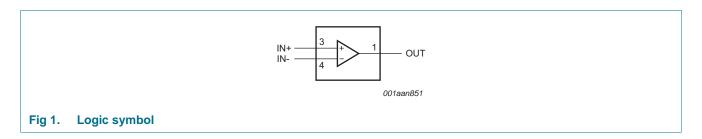
5. Marking

Table 2. Marking codes

Type number	Marking ^[1]
NCX2200GM	q1
NCX2200GW	q1

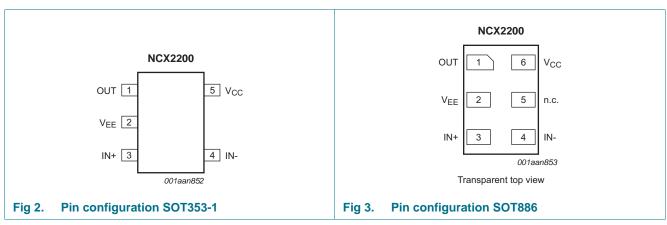
^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



7. Pinning information

7.1 Pinning



NCX2200

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Low voltage comparator

7.2 Pin description

Table 3. Pin description

Symbol	Pin		Description
	SOT353-1	SOT886	
OUT	1	1	comparator output
V_{EE}	2	2	supply voltage
IN+	3	3	comparator input (positive)
IN-	4	4	comparator input (negative)
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to V_{EE}.

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-	7.0	V
VI	input voltage	IN-, IN+ inputs	-0.2	$V_{CC} + 0.2$	V
t _{sc(o)}	output short-circuit time		<u>[1]</u> _	indefinite	S
T _{j(max)}	maximum junction temperature		-	+150	°C
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$	-	250	mW

^[1] The maximum total power dissipation must not be exceeded.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CC}	supply voltage	V_{CC} to V_{EE}				
		full spec operating range	1.6	-	5.5	V
		functional operating range	1.3	-	5.5	V
VI	input voltage		V_{EE}	-	V_{CC}	V
T _{amb}	ambient temperature		-40	-	+85	°C

Low voltage comparator

10. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. $V_{CC} = 1.6 \text{ V}$ to 5.5 V, $V_{EE} = 0 \text{ V}$; $V_{CM} = 0.5 V_{CC}$ unless otherwise specified.

Symbol	Parameter	Conditions			25 °C		–40 °C t	o +85 °C	Unit
				Min	Тур	Max	Min	Max	
V_{H}	hysteresis voltage			6	9	13	-	-	mV
		V _{CC} = 1.3 V		-	20	-	-	-	mV
V _{I(offset)}	offset input voltage		[1]	-30	0.5	+30	-30	+30	mV
		V _{CC} = 1.3 V	[1]	-	3	-	-	-	mV
V_{OH}	HIGH-level output voltage	$I_O = -0.5$ mA; $V_{CC} = 1.3$ V		-	1.24	-	-	-	V
		$I_{O} = -0.5 \text{ mA}; V_{CC} = 1.6 \text{ V}$		-	1.55	-	1.35	-	V
		$I_O = -3$ mA; $V_{CC} = 3.0$ V		-	2.85	-	2.7	-	V
		$I_{O} = -5 \text{ mA}; V_{CC} = 5.5 \text{ V}$		-	5.33	-	5.2	-	V
V_{OL}	LOW-level output voltage	I_{O} = 0.5 mA; V_{CC} = 1.3 V		-	0.05	-	-	-	V
		I_{O} = 0.5 mA; V_{CC} = 1.6 V		-	0.04	-	-	- m\ - m\ - m\ - m\ - m\ - m\ - v - v - v - v - v 0.25 v 0.3 v - v - mA - dB - dB	V
		$I_{O} = 3 \text{ mA}; V_{CC} = 3.0 \text{ V}$		-	0.14	-	-	0.3	V
		$I_{O} = 5 \text{ mA}; V_{CC} = 5.5 \text{ V}$		-	0.20	-	-	0.3	V
V_{CM}	common-mode voltage	$V_{CC} = 1.3 \text{ V to } 5.5 \text{ V}$		-	V_{EE} to V_{CC}	-	-	-	V
I _{OS}	output short-circuit current	V_{CC} = 5.5 V; V_{O} = V_{EE} or V_{CC}		-	68	-	-	-	mA
CMRR	common-mode rejection ratio	$\Delta V_{CM} = V_{CC}$		-	70	-	-	-	dB
PSRR	power supply rejection ratio	ΔV_{CC} = 1.95 V		45	80	-	-	-	dB
I _{IB}	input bias current			-	1.0	-	-	-	рА
I _{CC}	supply current			-	6.0	-	-	9.0	μΑ

^[1] Differential input switching level is guaranteed at the minimum or maximum offset voltage, minus or plus half the maximum hysteresis voltage.

11. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to V_{EE} (V_{EE} = 0 V); V_{CC} = 1.6 V to 5.5 V; V_{CM} = 0.5 V_{CC} unless otherwise specified.

Symbol	Parameter	Conditions	25 °C			Unit
			Min	Тур	Max	
t_{pd}	propagation delay	20 mV overdrive; C _L = 15 pF	-	0.8	-	μS
t _{THL}	HIGH to LOW output transition time	$V_{CC} = 5.5 \text{ V}; C_L = 50 \text{ pF}$	-	10	-	ns
t _{TLH}	LOW to HIGH output transition time	$V_{CC} = 5.5 \text{ V}; C_L = 50 \text{ pF}$	-	10	-	ns

^[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

^[2] Input signal: 1 kHz, squarewave signal with 10 ns edge rate.

Low voltage comparator

12. Graphs

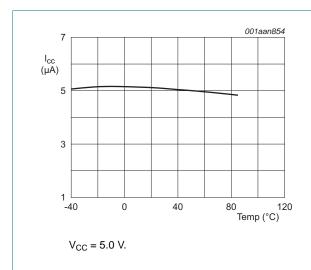
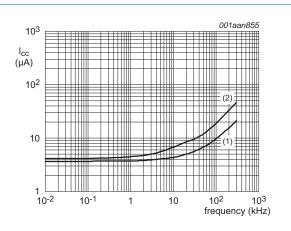


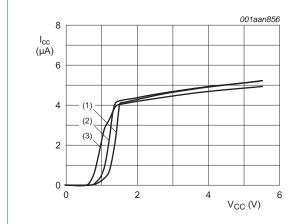
Fig 4. Supply current versus temperature



 T_{amb} = 25 °C; C_L = 15 pF.

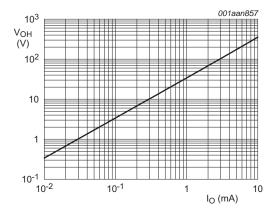
- (1) $V_{CC} = 2.7 \text{ V}.$
- (2) $V_{CC} = 5.0 \text{ V}.$

Fig 5. Supply current versus output transition frequency



- (1) $T_{amb} = -40 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = 85 \, ^{\circ}C$.

Fig 6. Supply current versus supply voltage



 $T_{amb} = 25 \, ^{\circ}C.$

 $V_{CC} = 5.0 \text{ V}.$

Fig 7. HIGH-level output voltage versus output current

Low voltage comparator

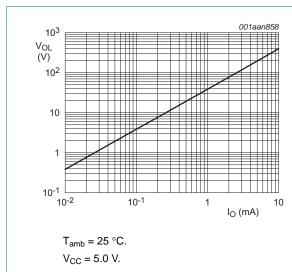
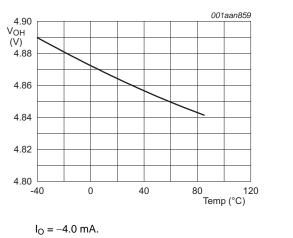


Fig 8. LOW-level output voltage versus output current



 $V_{CC} = 5.0 \text{ V}.$

Fig 9. HIGH-level output voltage versus temperature

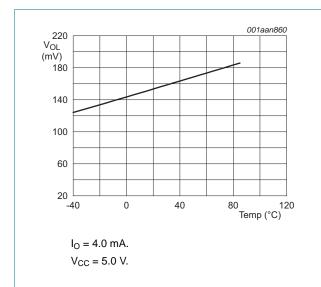
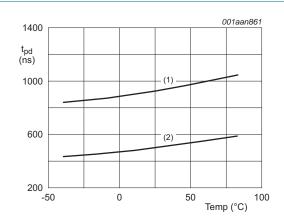


Fig 10. LOW-level output voltage versus temperature

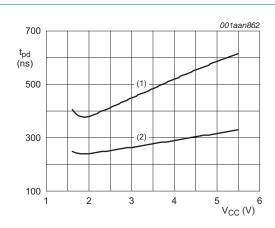


 $V_{CC} = 5.0 \text{ V}$; input overdrive = 50 mV.

- (1) t_{PLH}.
- (2) t_{PHL}.

Fig 11. Propagation delay versus temperature

Low voltage comparator



 T_{amb} = 25 °C; input overdrive = 100 mV.

- (1) t_{PLH}.
- (2) t_{PHL}.

Fig 12. Propagation delay versus supply voltage.

Low voltage comparator

13. Application information

13.1 Operating description

The NCX2200 is a single low voltage low power comparator. This device is designed for rail-to-rail input and output performance. This device consumes only 6 μ A of supply current while achieving a typical propagation delay of 0.8 μ s at a 20 mV input overdrive. This comparator is guaranteed to operate at a low voltage of 1.3 V up to 5.5 V. The common-mode input voltage range extends 0.1 V beyond the upper and lower rail without phase inversion or other adverse effects. This device has a typical internal hysteresis of 9.0 mV. This allows for greater noise immunity and clean output switching.

13.2 Output stage

The NCX2200 has a complementary P and N Channel output stage that has capability of driving a rail-to-rail output swing with a load ranging up to 5.0 mA. It is designed such that shoot-through current is minimized while switching. This feature eliminates the need for bypass capacitors under most circumstances. See <u>Figure 13</u>

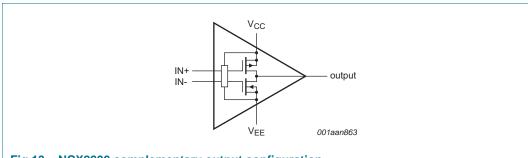


Fig 13. NCX2200 complementary output configuration

Low voltage comparator

13.3 Schmitt trigger oscillator

Figure 14 shows the NCX2200 configured as a Schmitt trigger oscillator.

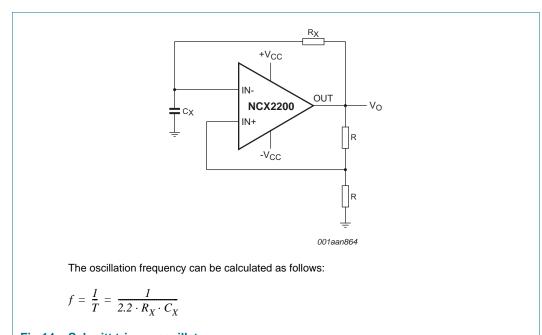
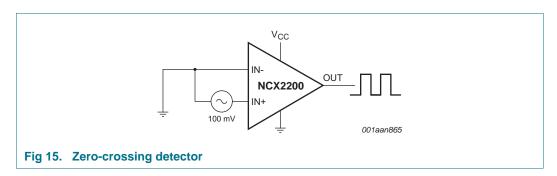


Fig 14. Schmitt trigger oscillator

13.4 Zero-crossing detector

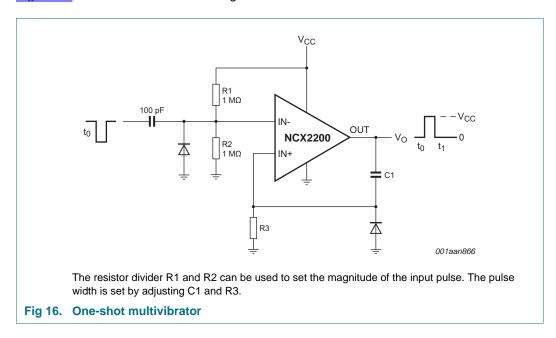
Figure 15 shows the NCX2200 configured as a zero-crossing detector.



Low voltage comparator

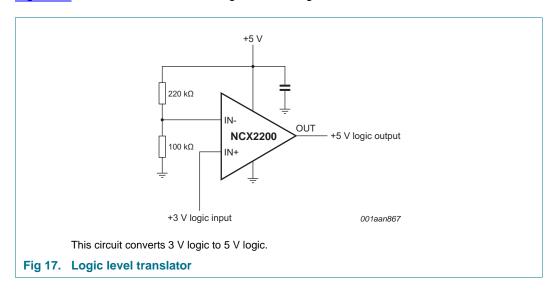
13.5 One-shot multivibrator

Figure 16 shows the NCX2200 configured as a one-shot multivibrator.



13.6 Logic level translator

Figure 17 shows the NCX2200 configured as a logic level translator.



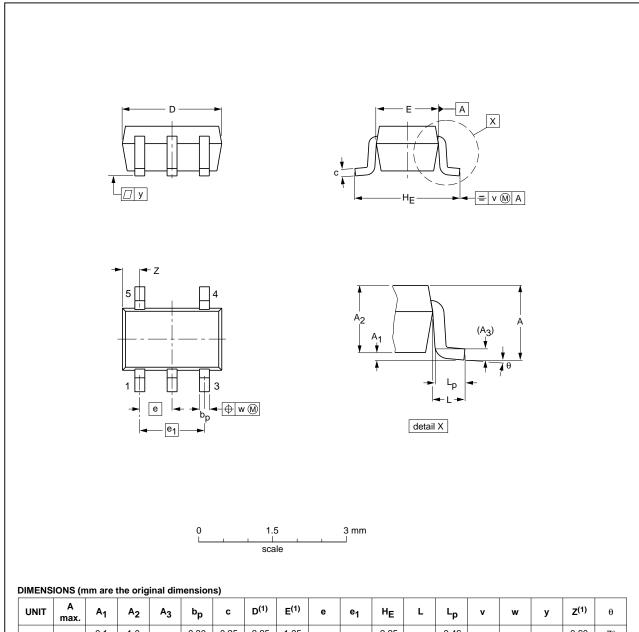
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Low voltage comparator

14. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	HE	L	Lp	v	w	у	Z ⁽¹⁾	θ	
mm	1.1	0.1 0	1.0 0.8	0.15	0.30 0.15	0.25 0.08	2.25 1.85	1.35 1.15	0.65	1.3	2.25 2.0	0.425	0.46 0.21	0.3	0.1	0.1	0.60 0.15	7° 0°	

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

	REFER	RENCES	EUROPEAN	ISSUE DATE	
IEC	JEDEC	JEITA		PROJECTION	1330E DATE
	MO-203	SC-88A			-00-09-01 03-02-19
_	IEC	IEC JEDEC	IEC JEDEC JEITA	IEC JEDEC JEITA	IEC JEDEC JEITA PROJECTION

Fig 18. Package outline SOT353-1 (TSSOP5)

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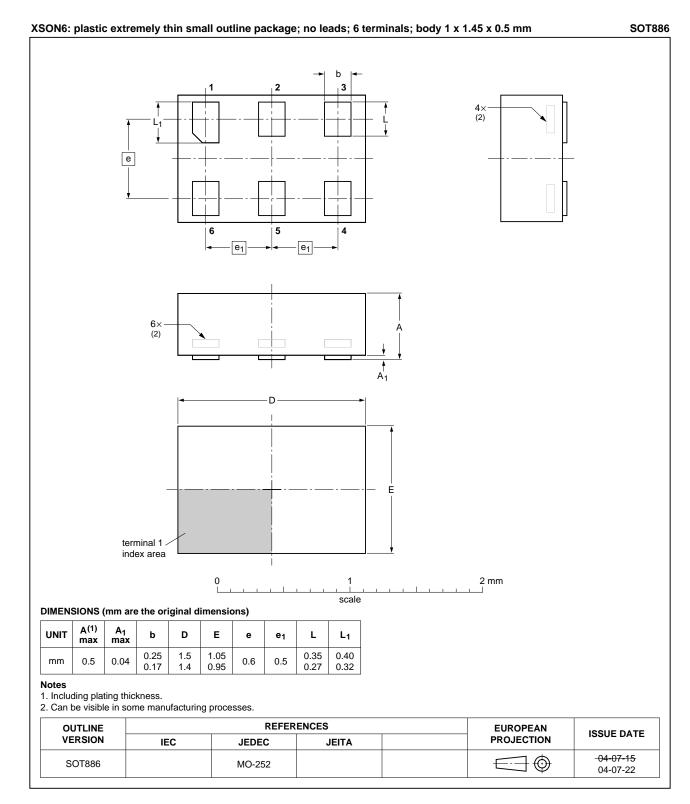


Fig 19. Package outline SOT886 (XSON6)

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Low voltage comparator

15. Abbreviations

Table 8. Abbreviations

Acronym	Description
CDM	Charged Device Model
ESD	ElectroStatic Discharge
HBM	Human Body Model

16. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NCX2200 v.1	20110322	Product data sheet	-	-

Low voltage comparator

17. Legal information

17.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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NCX2200

Low voltage comparator

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NCX2200 NXP Semiconductors

Low voltage comparator

19. Contents

1	General description
2	Features and benefits
3	Applications
4	Ordering information
5	Marking
6	Functional diagram 2
7	Pinning information
7.1	Pinning
7.2	Pin description
8	Limiting values 3
9	Recommended operating conditions 3
10	Static characteristics 4
11	Dynamic characteristics 4
12	Graphs
13	Application information 8
13.1	Operating description 8
13.2	Output stage 8
13.3	Schmitt trigger oscillator 9
13.4	Zero-crossing detector 9
13.5	One-shot multivibrator
13.6	Logic level translator10
14	Package outline
15	Abbreviations 13
16	Revision history 13
17	Legal information 14
17.1	Data sheet status
17.2	Definitions
17.3	Disclaimers
17.4	Trademarks15
18	Contact information
19	Contents 16

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